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			Examiner Name	ī	Unknown	
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Firm or Customer Number Attn: Justin Liu			24309 (Oustomer Number)		Reg. Number 51,959	
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PATENT

IN THE UNITED STATES PATENT OFFICE

Applicants:

Mingdeng Chen et al.

'Assignee:

Xilinx, Inc.

Title:

"Low Power Low Voltage Differential Signaling

Driver"

Serial No.:

10/655.637

File Date:

September 5, 2003

Examiner:

Unknown

Art Unit:

Unknown

Docket No.:

x-1501 US

COMMISSIONER FOR PATENTS

P.O. Box 1450

Alexandria, VA 22313-1450

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Dear Sir:

Pursuant to 37 C.F.R. 1.56, Applicants bring to the attention of the Examiner the two (2) references listed in the attached Form PTO-1449. A copy of each is enclosed herein.

This Information Disclosure Statement is being filed under 37 CFR 1.97(b) prior to the receipt of a first office action.

Citation of the above documents shall not be construed as an admission that the documents are necessarily prior art with respect to the instant invention. Citation of the above documents shall not be construed as a representation that a search has been made other than as described above. Also, the citation of the above documents shall not be construed as an admission that the information cited herein is, or is considered to be, material to patentability as defined in §1.56(b).

Respectfully submitted,

Justin Liu

Attorney for Applicants

Reg. No. 51,959

I hereby certify that this correspondence is being deposited with the United States Postal Service as **first class mail** in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, or September 18, 2003

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Con	Complete if Known					
Application / Conf. No.	10/655,637 /					
Filing Dat	September 05, 2003					
First Named Inventor	Mingdeng Chen					
Art Unit	Unknown					
Examiner Name	Unknown					
Attorney Docket Number	X-1501 US					

		OTHER NON PATENT LITERATURE DOCUMENTS	
Examiner Initials *	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T²
		SHAHRIAR JAMASB, A 622MHz Stand-alone LVDS Driver Pad in 0.18-um CMOS, IEEE, 3 Park Avenue, 17th Floor, New York, NY., 10016-5997, 2001, Conexant Systems and University of San Diego, San Diego, California.	
		ANDREA BONI, LVDS I/O Interface for Gb/s-per-Pin Operation in 0.35-um CMOS, IEEE Journal Of Solid-State Circuits, April 2001, Vol. 36, No. 4, IEEE, 3 Park Avenue, 17th Floor, New York, NY., 10016-5997,.	
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